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PATENT NO.

7,225,098

APPLICATION NO.

10/535,064

ISSUE DATE

May 29, 2007

INVENTOR(S)

Xavier Robert

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 1, line 62, should read:

Thus, standard IEEE-ISTO-5001, in preparation, pro-"

Column 2, line 39, should read:

toring circuit according to standard IEEE-ISTO-5001 which

Column 6, lines 53-56 should read:

type (ADDR). Block A can then be divided into a sub-block A' intended to only store data of I-CNT type, and a sub-block A" intended to only store addresses ADDR. The boxes of sub-blocks A' and A" are provided each to only respectively store data I-CNT or an address ADDR. Subblock A' will

MAILING ADDRESS OF SENDER (Please do not use customer number below): James H. Morris WOLF, GREENFIELD & SACKS, P.C. Federal Reserve Plaza 600 Atlantic Avenue Boston, Massachusetts 02210-2206